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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/731,442

12/10/2003

Jun Koyama

12732-188001 / US6808

5168

26171

7590

06/30/2006

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EXAMINER

PATEL, NITIN

ART UNIT

PAPER NUMBER

2629

DATE MAILED: 06/30/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/731,442	KOYAMA, JUN	
	Examiner	Art Unit	
	Nitin Patel	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 December 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-30 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-30 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>6/28/04</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-30 rejected under 35 U.S.C. 102(e) as being anticipated by (JP 2004-077567).

As per claim 1, Kazutaka shows a display device comprising: a plurality of pixels each having a switching element (in drawing paper 1-1 figure B) and a light-emitting element (In fig.b drawing page 1-1) substrate in matrix; a plurality of source signal lines (in fig.b page 1-1) for one pixel column ((in fig.b); and one gate signal line for one pixel row, wherein the switching element has an input terminal, an output terminal, and a control terminal; wherein the input terminal is electrically connected to any one of the plurality of source signal lines; wherein the output terminal is electrically connected to the light-emitting element; and wherein the control terminal is electrically connected to the gate signal line(in section 0019 and 0020 and fig.b).

As per claim 2, Kazutaka shows a display device (In fig.b drawing page 1-1) comprising: a plurality of pixels each having a switching element and a light-emitting element over a substrate in matrix (in fig.B page 1-1); a plurality of source signal lines

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(in fig.B source lines Sai....Sdi) for one pixel column (in fig.B element E-1...E-4); one gate signal line for one pixel row; and a plurality of source signal line driver circuits each electrically connected to at least one of the plurality of source signal lines(In fig.B), wherein the switching element(In fig.B element shown as transistors) has an input terminal, an output terminal, and a control terminal(in fig.b); wherein the input terminal is electrically connected to any one of the plurality of source signal lines; wherein the output terminal is electrically connected to the light-emitting element; and wherein the control terminal is electrically connected to the gate signal line(in fig.B).

As per claims 3,19 Kazutaka teaches a display device (In fig.B page 1-1) comprising: a plurality of pixels each having a switching element (in fig.b page 1-1) and a light-emitting element on over a substrate in matrix; a plurality of source signal lines for one pixel column; and gate signal lines each for one pixel row, and a gate signal line driver circuit driving the gate signal lines simultaneously (In abstract and in fig.B page 1-1) wherein the switching element has an input terminal, an output terminal, and a control terminal; wherein the input terminal is electrically connected to any one of the plurality of source signal lines; wherein the output terminal is electrically connected to the light-emitting element; and wherein the control terminal is electrically connected to one of the gate signal lines(In fig.B page 1-1 and in section 0019-0020).

As per claim 4, Kazutaka shows wherein each of the plurality of source signal line driver circuits is a current output type source signal line driver circuit 9in section 0003).

As per claim 5, Kazutaka shows wherein each of the plurality of source signal

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line driver circuits is formed using a thin film transistor (in fig.B page 1-1).

As per claim 6, Kazutaka shows wherein each of the plurality of source signal line driver circuits is formed over the same substrate as the switching element (in fig.B page 1-1).

As per claim 7, Kazutaka shows wherein each of the plurality of source signal line driver circuits is mounted over a semiconductor chip (in section 0015).

As per claim 8-17,20-28 Kazutaka shows wherein a each of the plurality of the source signal line driver circuits is disposed on a side of a region where the plurality of pixels are disposed and at least one of the plurality of source signal line driver circuits drives any one of the plurality of source signal lines and wherein each of the plurality of source signal line driver circuits is a formed using transistors having a single polarity and the gate signal line driver circuit is formed using a thin film transistor (in fig.b page 1-1 and in section 0018) and mounterd on a single chip using EL element (In fig.b and in section 0003 and section 0014,0015) .

As per claims 18,29,30 Kazutaka shows wherein the electronic apparatus is one selected from the group consisting of a video camera, a digital camera, a notebook personal computer, a mobile computer, a portable image reproducing device provided with a recording medium, a head mounted display, a game machine, a car navigation system, a personal computer, a portable information terminal, a mobile phone, an electronic book, a folding portable display device, and a wristwatch type display device (in fig.a-h using different devices).

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677.

The examiner can normally be reached on 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nitin Patel
Examiner
Art Unit 2629

